

[ 06 ELECTRICAL & ELECTRONIC ]  
COLLEGE : GOA COLLEGE OF ENGINEERING

\*\* RC2007-08 \*\*  
EXAM YEAR : NOV-2019

SEAT NO.	P.R. NO.	ARE PRO CD. VI.	SEX	NAME OF THE CANDIDATE	THEORY MAX. 100 MIN. 040	SESSIONAL 25	TOTAL 125 50	PRACTICAL 50 OR 25 20 OR 10	DRAL 50 OR 25 20 OR 10	REMARKS MAX.850 MIN.340
2024	201509090	(E)	N	M ARUN SINGH RATHORE						** RESULT WITHHELD - (SEM. 6 NOT PASSED) **
				VLSI CIRCUIT DESIGN	030	005	035F		022E	
				ELECTRICAL POWER SYSTEM II	040	000	040F			
				ADVANCED CONTROLLED DRIVES	050	000	050E			
				NEURAL NETWORKS & FUZZY LOGIC	000	000	000A		020+	
				FLEXIBLE AC TRANSMISSION SYSTEM PROJECT	049	000	049E	\$1	035+	
						024+			030+	
2025	201406897	(E)	N	M KHARAT RAJESH RATO						** RESULT WITHHELD - (SEM. 6 NOT PASSED) **
				VLSI CIRCUIT DESIGN	026	005	031F		028+	
				ELECTRICAL POWER SYSTEM II	042	013	055+			
				ADVANCED CONTROLLED DRIVES	050	010	060+			
				ELECTRICAL ESTIMATION & COSTING	062	010	072+		030+	
				FLEXIBLE AC TRANSMISSION SYSTEM PROJECT	050	010	060+		035+	
						015+			040+	
2026	201203680	(E)	N	M VIJAYAN TONNY						
				VLSI CIRCUIT DESIGN	049	004	053P		025+	
				ELECTRICAL POWER SYSTEM II	048	017	065+			
				ADVANCED CONTROLLED DRIVES	044	015	059+			
				ELECTRICAL ESTIMATION & COSTING	046	021	067+		020+	
				FLEXIBLE AC TRANSMISSION SYSTEM PROJECT	050	017	067+		029+	
						014+			032+	
										431 P
2221	201509073	(E)	N	M GAUNKER ANIRUDHA						** RESULT WITHHELD - (SEM. 6 NOT PASSED) **
				VLSI CIRCUIT DESIGN	000	010	010A		025+	
				ELECTRICAL POWER SYSTEM II	040	011	051+			
				ADVANCED CONTROLLED DRIVES	043	010	053+			
				ELECTRICAL ESTIMATION & COSTING	040	011	051+		020+	
				FLEXIBLE AC TRANSMISSION SYSTEM PROJECT	049	012	061+		027+	
						014+			030+	
2222	201509094	(E)	N	M GAWDE AKSHAY GUND						** RESULT WITHHELD - (SEM. 5 NOT PASSED) **
				VLSI CIRCUIT DESIGN	000	000	000F		020+	
				ELECTRICAL POWER SYSTEM II	040	010	050+			
				ADVANCED CONTROLLED DRIVES	064	010	074+			
				ELECTRICAL ESTIMATION & COSTING	031	010	041F		020+	
				FLEXIBLE AC TRANSMISSION SYSTEM PROJECT	052	012	064+		026+	
						012+			028+	

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SEAT P.R. NO.	ARE PRO	SEX	NAME OF THE CANDIDATE	THEORY	SESSIONAL	TOTAL	PRACTICAL	ORAL	REMARKS
NO.	CD. VI.			MAX. 100	25	125	50 OR 25	50 OR 25	MAX.850
PAPER DESCRIPTION				MIN. 040		50	20 OR 10	20 OR 10	MIN.340


2223	201509060	(E)	N M	NAIK SAHIL ULHAS					** RESULT WITHHELD - (SEM. 5 NOT PASSED) **
				VLSI CIRCUIT DESIGN	000	000	000F	020+	
				ELECTRICAL POWER SYSTEM II	041	010	051+		
				ADVANCED CONTROLLED DRIVES	048	010	058+		
				ELECTRICAL ESTIMATION & COSTING	048	016	064E	021+	
				FLEXIBLE AC TRANSMISSION SYSTEM	045	011	056+	026+	
				PROJECT		013+		028+	

READ BY  CHECKED BY 

11 3 FEB 2020  
DECLARED ON

  
ASST. REGISTRAR-E (PROF)

  
ASST. REGISTRAR (EXAM)

 13/2  
CONTROLLER OF EXAMINATION